Title **(Metodyka projektowania i technika realizacji)**	Code **1018051210108430303**
Field **Electronics and Telecommunications**	Year / Semester **1 / 2**
Specialty **-**	Course **core**
Hours Lectures: **2** Classes: **-** Laboratory: **-** Projects / seminars: **20**	Number of credits **0**
	Language **polish**

Lecturer:

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Status of the course in the study program:

Obligatory course for students of Electronics and Telecommunications,

Assumptions and objectives of the course:

Digital and multimedia system design. Introduction to Field Programmable Gate Arrays devices (FPGA).

Contents of the course (course description):

FPGA, programmable devices.
SoC Embedded systems (System-on-Chip).
Communication bus standards and NoC networks (Network-on-Chip).
FPGA synthesis methodology.
Digital interfaces.
Testing and verification methodology.

Introductory courses and the required pre-knowledge:

Knowledge of logics and digital circuits design.

Courses form and teaching methods:

Lecture, laboratory work

Form and terms of complete the course - requirements and assessment methods:

Written tests.

Basic Bibliography:

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Additional Bibliography:

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